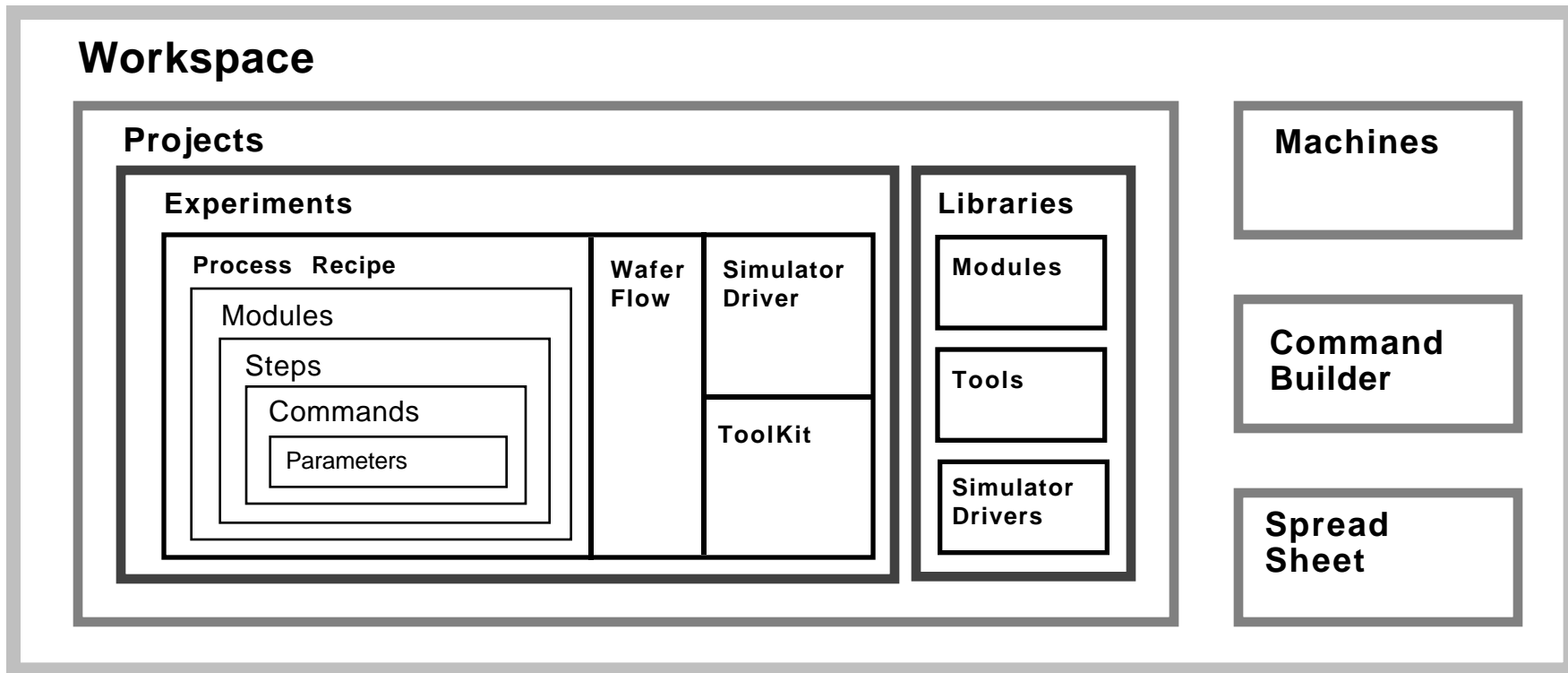
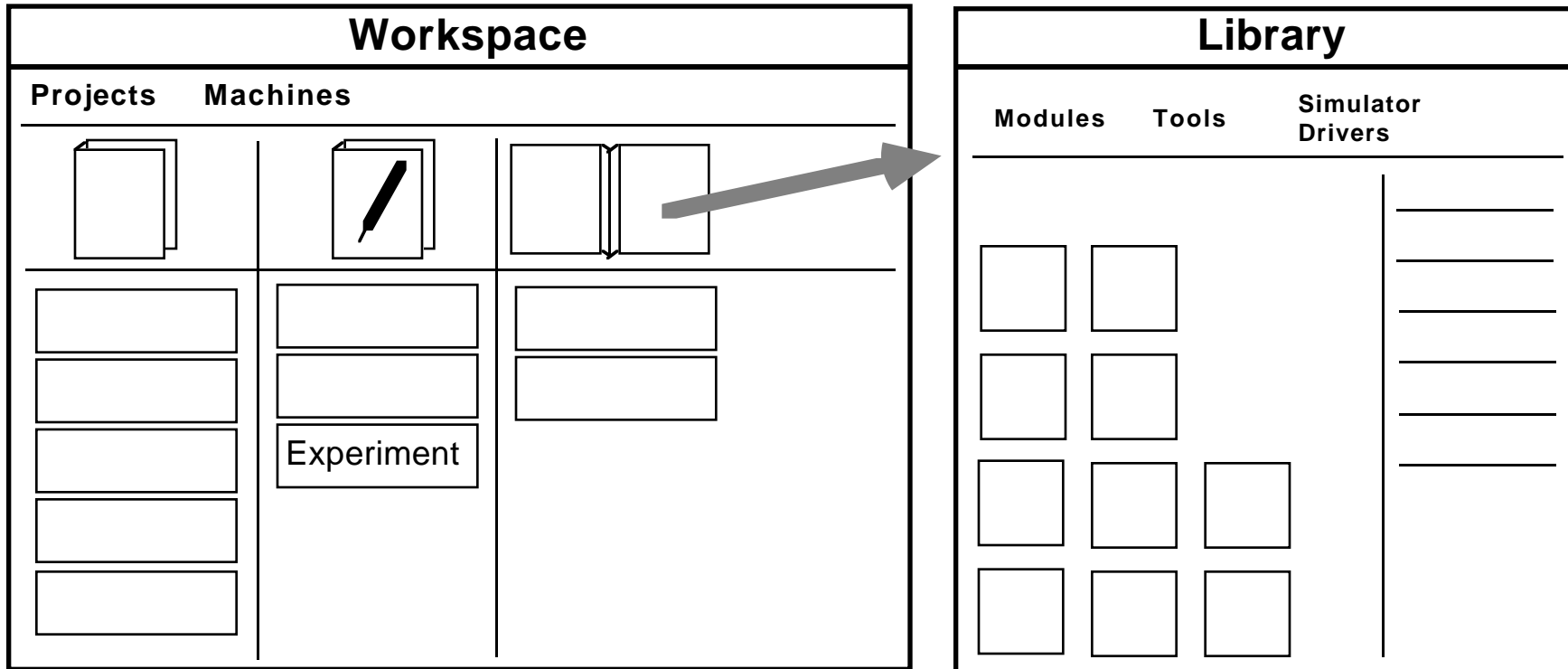


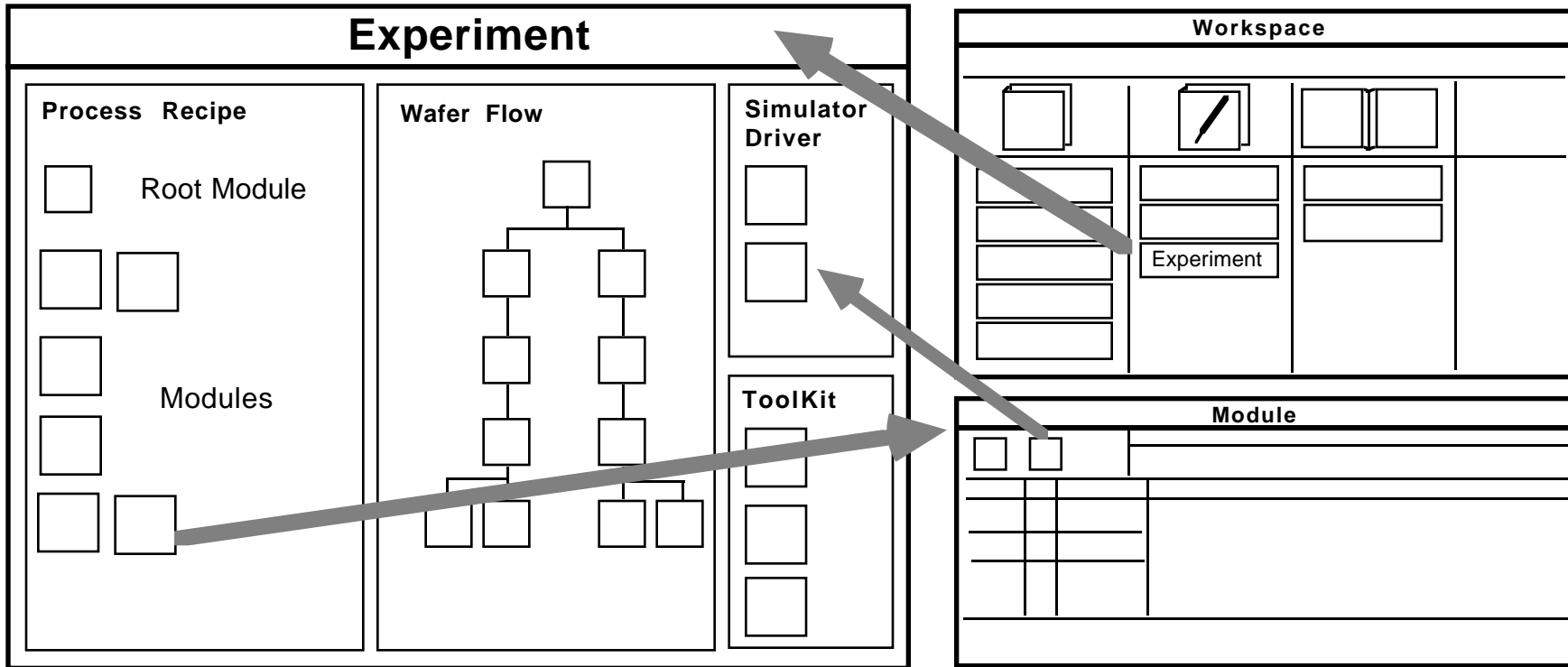
WorkBench Structure



WorkBench Projects and Libraries



WorkBench Experiments

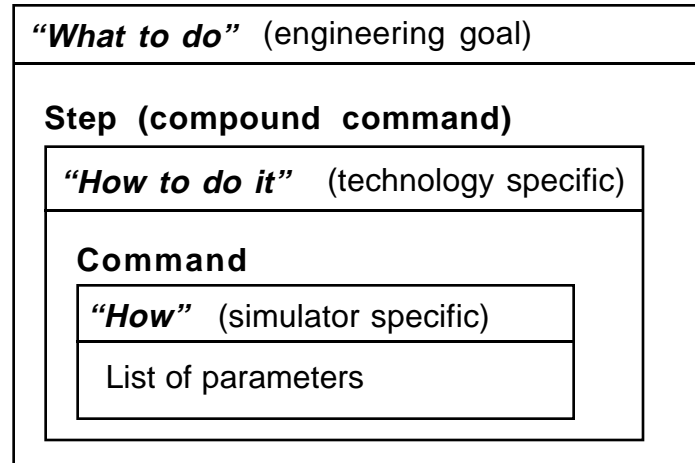


WorkBench Modules

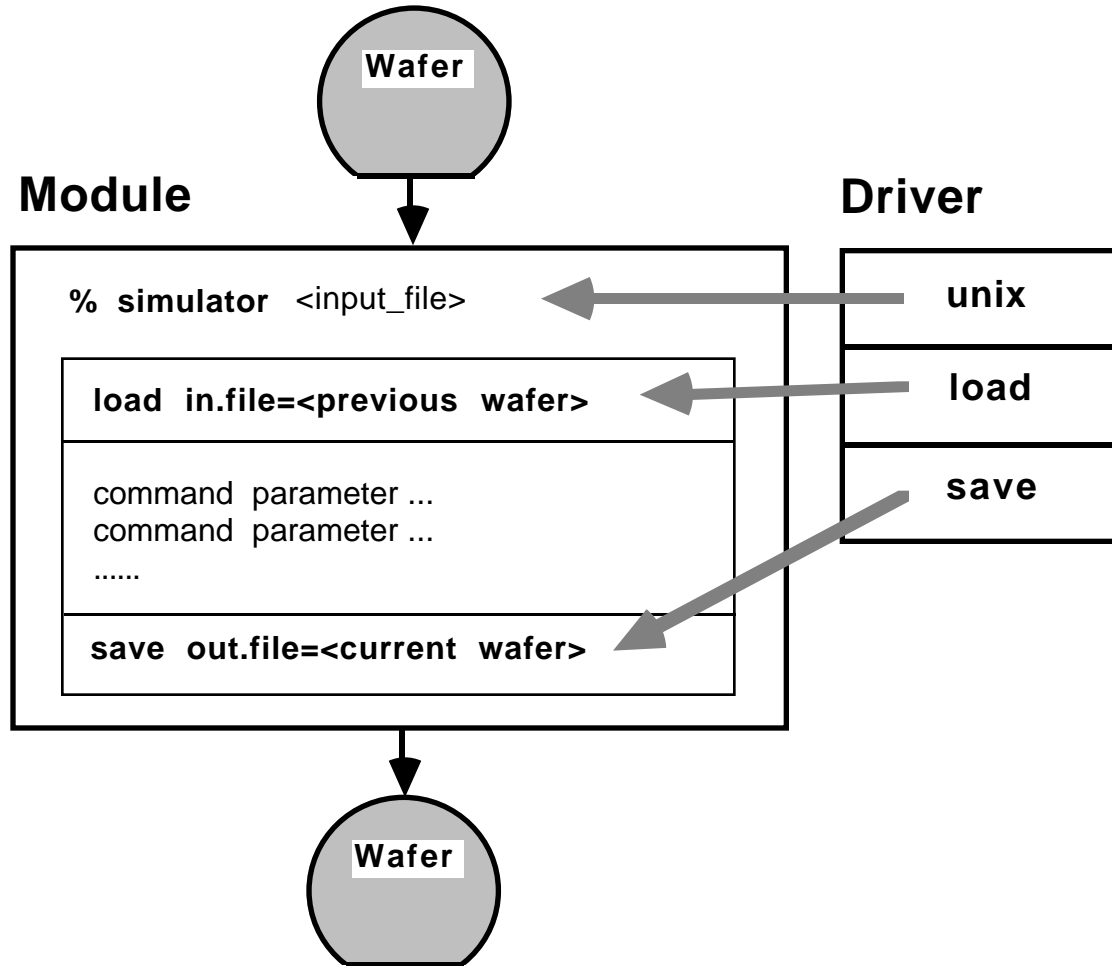
Module			
Icon	Driver	Name	
<input type="text"/>	<input type="text"/>	Annotation	
Level	Command	Parameter	
0	Step_1	Parameter_1 Parameter_2	
1	▽ Step_2		
2.1	Command		
	▶ Step_3		
Parameter <input type="text"/>			
Label	<input type="text"/>	Min <input type="text"/>	Max <input type="text"/> <input type="checkbox"/> Control

Module Concept

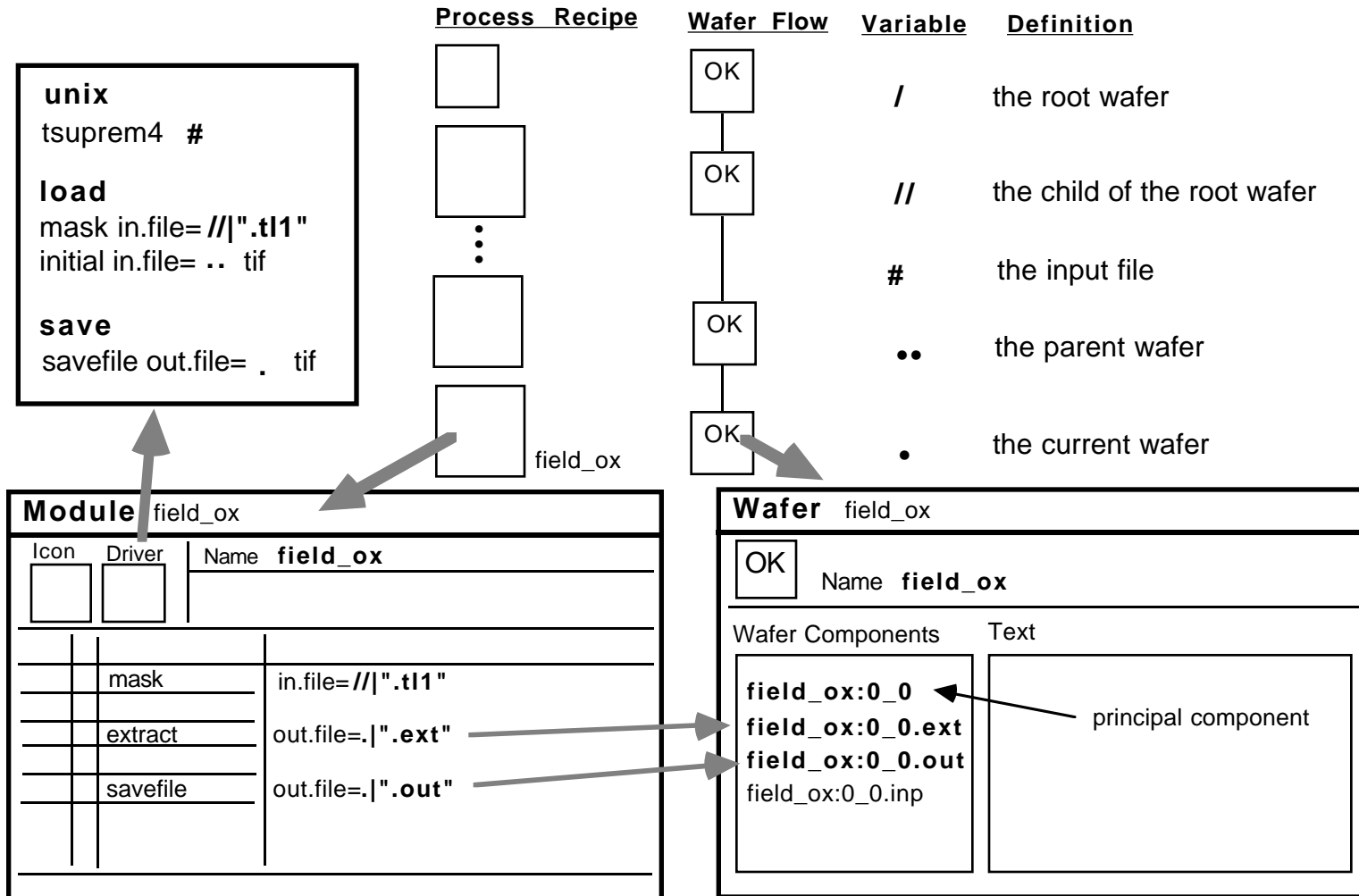
Module



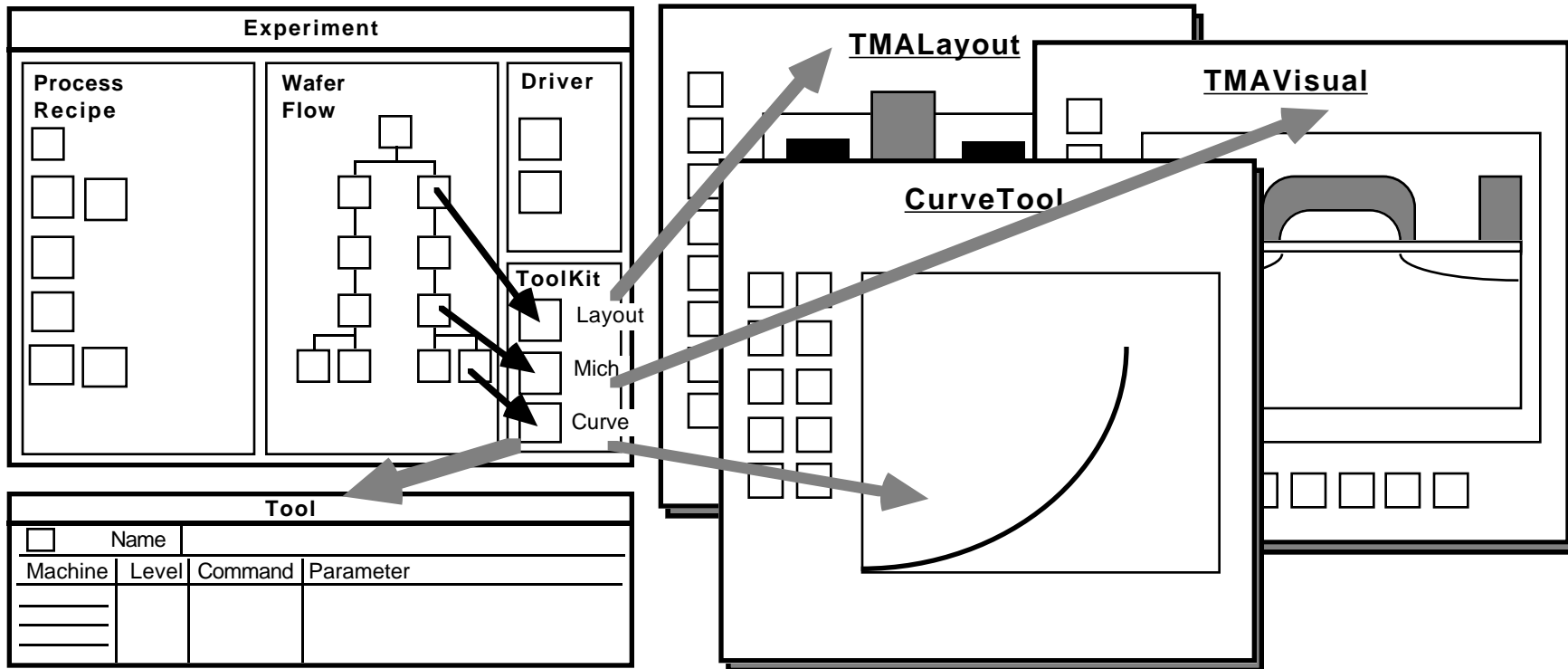
WorkBench Drivers



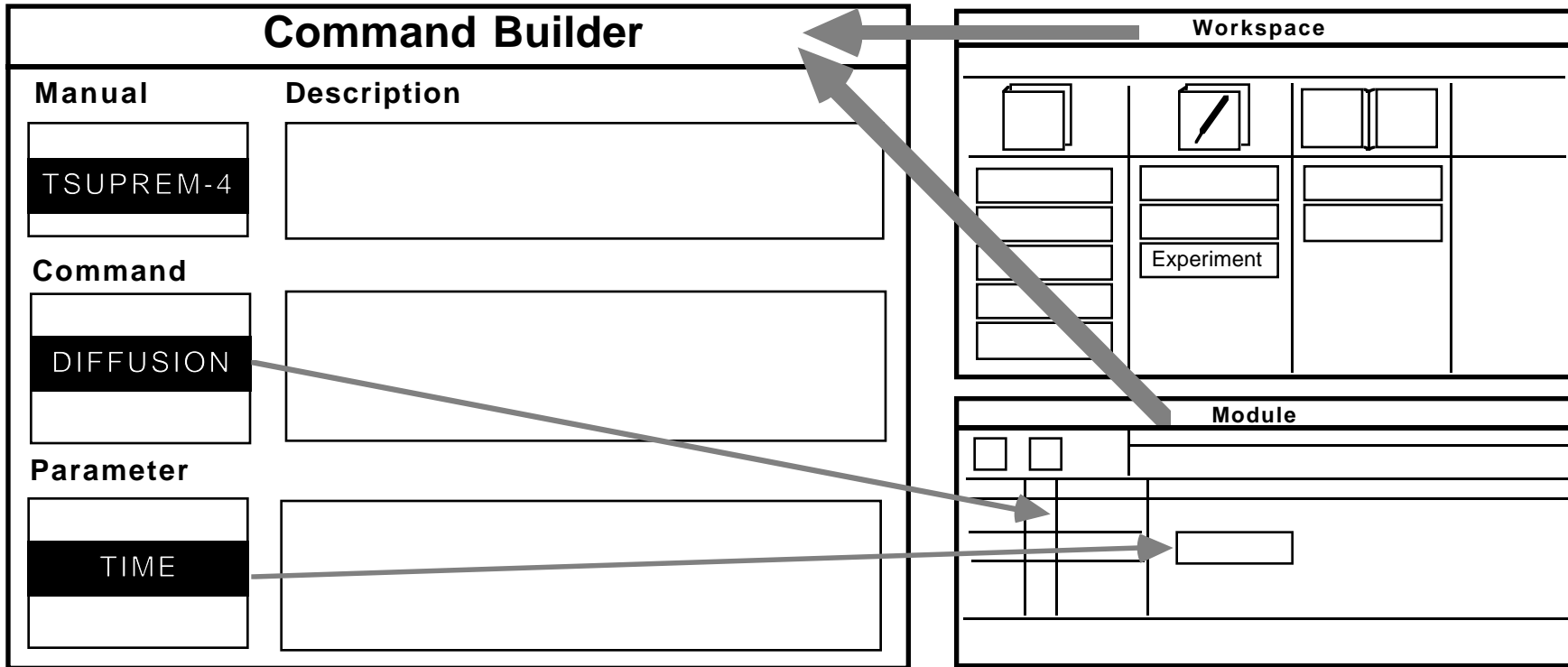
WorkBench Variables and Wafer Components



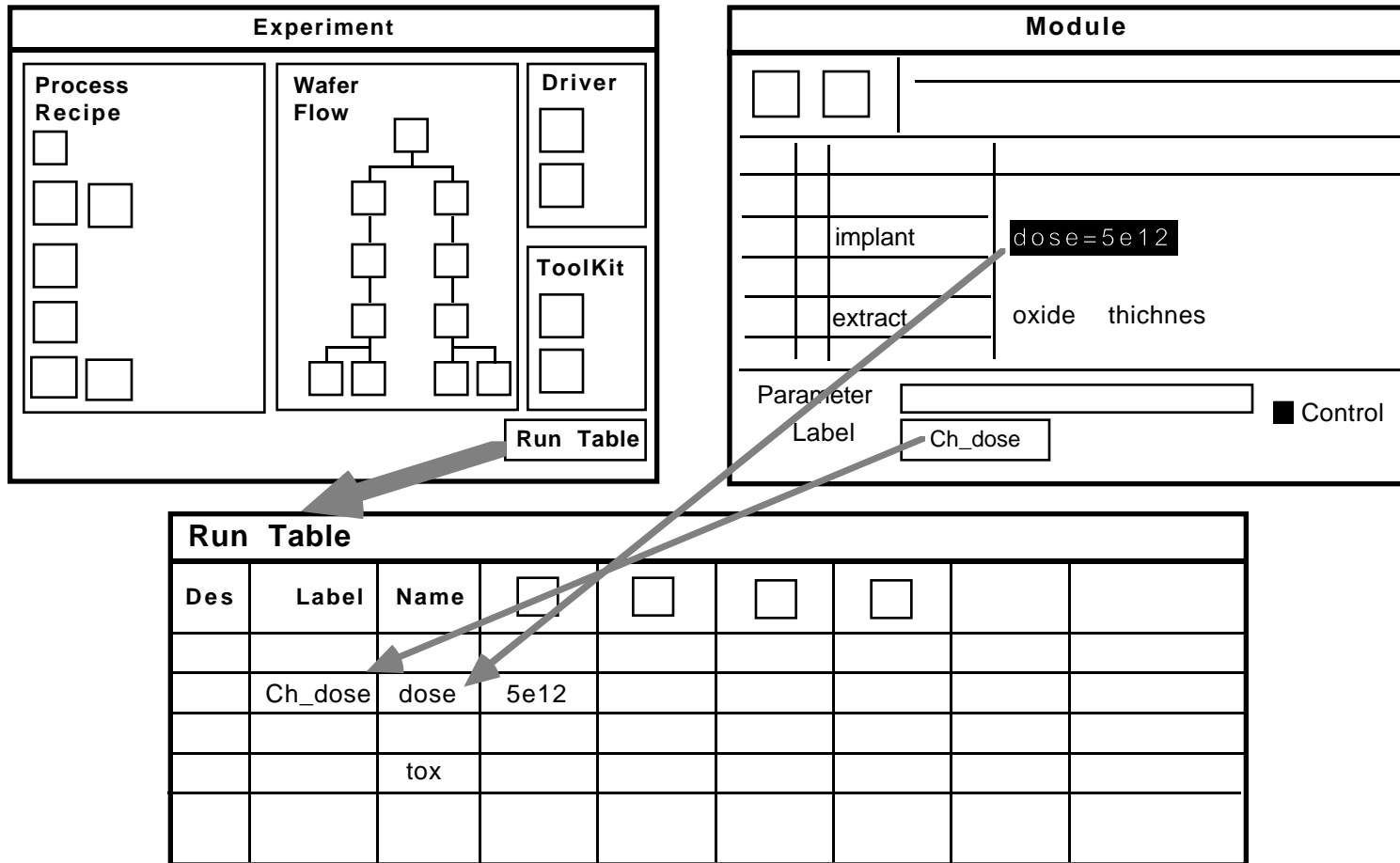
WorkBench ToolKit





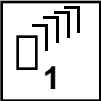


WorkBench Command Builder



WorkBench Run Table

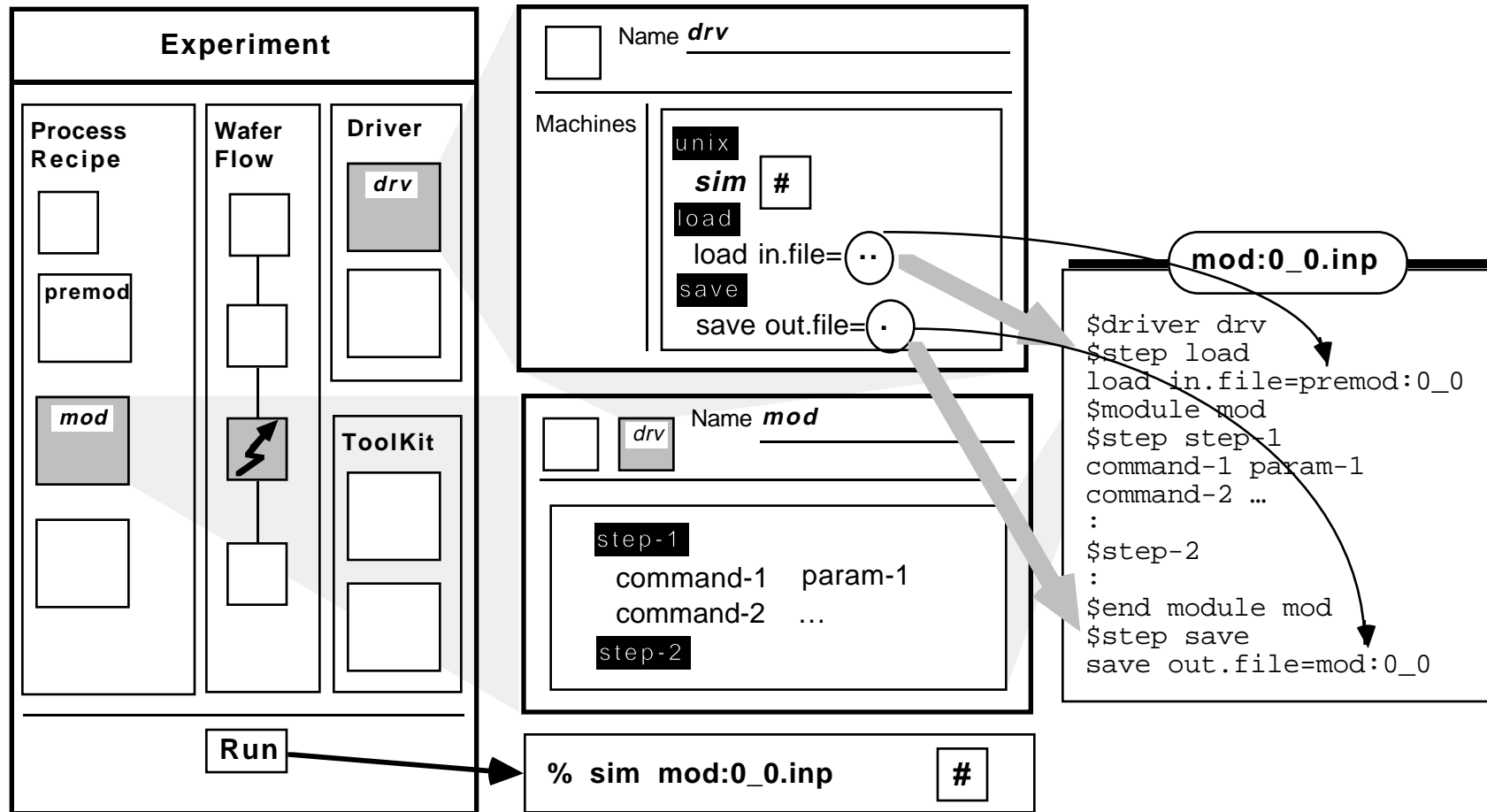


Wafer State and Simulation Status

<u>Icon</u>	<u>Color</u>	<u>Simulation Status</u>
	Yellow	Module ready for simulation
	Purple	Module being simulated
	Blue	Module waiting for available CPU to be simulated
	Black	Module simulation failed
	Green	Module completed successfully

- ❑ **To make a wafer “green”** — at the completion of the Module simulation, a “Principal Component” file with the name “**<module_name>:0_0**” must be created

Creating a Simulator Driver



Creating a Tool

