Design and Innovation Project

Virtual Transistor Fabrication and Characterization

Grouping and Group Leader Assignment

Group	Sub grp		Student Name	Usrid	Group Leader of the Week				
					1	2	3	4	5
1	a	1	CHEN CHAO	f6401	●				
		2	CHEONG ZHAOMING GARY			●			
	b	1	MA QINGQIANG	f6402			●		
		2	SHIVA RAJ S/O RATHA KRISHNAN					●	
2	a	1	LIM ZI JIA	f6403	●				
		2	LOH WEN JIN			●			
	b	1	HAN CHEE SIEW	f6404			●		
		2	TAN YIT SENG EDWIN					●	
3	a	1	CHAN EE SIN EVANGELINE	f6405	●				
		2	TANG WAN LIN DAPHNE			●			
	b	1	KELVIN LIN BAOSHAN	f6406			●		
		2	LIM HENRY					●	
4	a	1	CHUA YONG KIANG	f6407	●				
		2	LIM YEE FATT			●			
	b	1	EI MON THAN	f6408			●		
		2	SHWE SIN WIN					●	
5	a	1	CHUA BOR JENQ	f6410	●				
		2	YAP WEI RONG			●			
	b	1	CHIN WEI JIE EDWIN	f6411			●		
		2	TAN HONG CHUN					●	

Project Milestones

Task	Week				
	1	2	3	4	5
Literature search/general reading	—				
Technical reading (process/device/DOE)	—	—			
Familiarization with TCAD tools	—	—			
Design conceptualization/specification	—	—			
Design implementation/simulation		—	—	—	
Data collection/analysis/plotting		—	—	—	
Design optimization			—	—	
Design application/generalization			—	—	—
Technopreneurship exploration				—	—
Preliminary/final competitions					—
Documentation/presentation	—	—	—	—	—