Senior/Process Integration Engineers
The Job:
• Perform daily monitoring of EWS yield with in-depth analysis
• Senior/Device Engineers
• Ability to work in a cross functional team
• Sustain manufacturing yield and quality
• Implement projects, lead a team to improve the process
• Troubleshoot process deviation and equipment malfunctions
• Excellent communication skills
• Able to characterize elementary components using bench testing preferred
• Knowledge of Statistical Process Control & Design Of Experiments concepts preferred
• Good interpersonal and communication skills

Requirements:
• Degree in Material Science or Electronics/Chemical Engineering with minimum 3 - 5 years’ experience
• Experience in the fields of process integration, yield enhancement, module process or technology transfer will be an advantage
• Technical skills including problem-solving, statistical data analysis & DOE
• Understanding of E-test & Device physic required
• Good understanding in various failure analysis tools (SEM, FIB, TEM) and electrical characterisation
• Strong communication & presentation skills
• Ability to work in a cross functional team

Senior/Device Engineers
The Job:
• Perform daily monitoring of EWS yield with in-depth analysis
• Handle daily EWS scrap management with fast feedback & corrective actions through failure analysis
• Ensure smooth transfer of new technologies & products from divisions
• Involved in process improvement to enhance process or test yield

Requirements:
• Masters/ Degree in Science (Physics) or Electronics/Electrical Engineering with 3 - 5 years’ relevant hands-on experience
• Preferably with related experience in device physics, process integration, electronics testing & reliability testing analysis
• Understanding of E-test & Device physic required
• Good understanding in various failure analysis tools (SEM, FIB, TEM) and electrical characterisation
• Knowledge of Statistical Process Control & Design Of Experiments concepts preferred
• Experience interfacing in an MES environment
• Able to characterize elementary components using bench testing preferred
• Excellent communication skills

Senior/Process Engineers
The Job:
• Perform development and process transfer of sub-quarter or sub-micron advanced technologies from R&D to manufacturing
• Set up SPC for developed processes
• Install equipment and setup process specifications
• Perform and characterise process and equipment system to maintain and control process parameters
• Troubleshoot process deviation and equipment malfunctions to improve process quality and productivity
• Implement projects, lead a team to improve the process capabilities and reliability
• Sustain manufacturing yield and quality

Requirements:
• Degree in Material Science or Electronics/Chemical Engineering, preferably with relevant working experience in wafer fab industry
• Ability to work in a cross functional team
• Technical skills including problem-solving, statistical data analysis and design of experiment
• Able to perform 12 hours rotating shift

Senior/Product Engineers
The Job:
• Undertake product characterization, product transfer and test program writing and debugging
• Liaise with Divisional Product Engineering and local support groups to ensure smooth production
• Embark on continuous improvement projects to enhance testing methods, quality and productivity
• Work closely with Device and Process Engineer to improve yield

Requirements:
• Degree in Electrical / Electronics/Microelectronics Engineering
• Preferably with at least 1 year’s relevant experience in semiconductor product or test engineering
• Experience in LTX (Gyncho or Fusion) testers will be an added advantage
• Good interpersonal and communication skills

Senior/Equipment Engineers
The Job:
• Lead the equipment engineering support team for in-depth technical analysis on equipment and process related matters
• Carry out effective planning for corrective and preventive measures to maintain high equipment reliability and process stability with minimal resource requirements
• Perform semiconductor manufacturing equipment start-up, evaluation, installation and maintenance
• Analyse equipment failure pareto to carry out continuous improvement projects to achieve higher equipment uptime

Requirements:
• Degree in Electrical/Electrical/Mechtronics Engineering with 3 - 5 years’ relevant working experience, preferably in wafer fab industry
• Self-motivated with sound knowledge of equipment interfacing in an MES environment
• Good team player

Assistant Maintenance Engineers
The Job:
• Carry out equipment maintenance to ensure performance consistency according to process specifications
• Ensure high level of preventive maintenance programmes are implemented and strive for high uptime and reduce repair time

Requirements:
• Diploma in Electronics/Electrical/Mechtronics Engineering
• Higher NITEC holders with 3 - 4 years’ relevant experience will also be considered
• Ability to perform 12 hours rotating shift

Manufacturing Supervisors
The Job:
• Manage production area to meet production targets for cycle time, output and yield
• Lead and manage a team of Wafer Fab Specialists and Technical Assistants/Specialist in the day to day operations to meet daily targets
• Present daily production indices in module meetings and highlight key priorities for the Engineering teams to focus on
• Identify and resolve preventive maintenance issues and equipment capacities/constraints related issues
• Ensure safety, discipline and housekeeping compliance by all personnel and take actions on violators

Requirements:
• Degree/Diploma in any discipline, preferably with work experience in manufacturing environment
• Excellent interpersonal and communication skills
• Good analytical and planning skills
• Proven leadership ability will be an asset
• Team player
- Ability to perform 12 hours rotating shift

Process Control Engineer
The Job:
• Set up the in-line defectivity monitoring systems
• Identify key sources of defects
• Co-ordinate defectivity reduction activities with process engineering and equipment groups

Requirements:
• Degree in Science/Electronics/Electrical Engineering with 3 - 5 years’ relevant work experience in wafer fab industry
• Familiar with TENOR 7700 and LEICA review stations
• Experience in KLA defectivity tools

System Engineers
The Job:
• Design, develop, and deploy new IT projects through all fabs in AMK
• Conciliate short term imperatives, responsiveness to users’ needs with project management and medium to long term strategies
• Work in a team, to ensure effective communication with users and other members of the team

Requirements:
• Bachelor’s Degree in IT or Engineering field with minimum 1 year’s working experience in the semiconductor industry
• Excellent knowledge of New Technologies, Internet based architecture and Linux environment
• Good skills in Linux administration, Sun Solaris, HPUX and Windows NT
• Experience in Scripting Language like Perl and Shell
• Experience in Web Development with Java, JSP, Servlet, JavaScript, Php, NET, XML and HTML
• Familiar with Web Environment like Apache, Tomcat and Cocoon
• Good knowledge of Relational Databases like Oracle, MySql and Informix
• Fluent English is mandatory

Yes, an international cast of very talented professionals at STMicroelectronics is taking our business to greater heights. Look forward to growing and sharing learnings with this global team. With overseas assignments, you’ll sharpen your expertise even further. All within a global semiconductor giant that is firmly entrenched in Singapore, with the vision to become an unrivalled leader in this high-tech industry. Translate all these into career opportunities, and you can be sure that STMicroelectronics is the choice employer for you.

If you are unable to attend the walk-in interview, you may e-mail your resume indicating current & expected salaries by 22 May 2004 to:

STMicroelectronics Pte Ltd
28 Ang Mo Kio Industrial Park 2 Singapore 569508
E-mail: whr.recruitment@st.com

(All applications will be treated with the strictest confidentiality. Only shortlisted candidates will be notified.)
IC DESIGN ENGINEERS
(Digital)
The Job:
• Responsible for the design and verification of IC's and involved in
  entire IC development process, from specification to silicon
evaluation
Requirements:
• Masters/Degree in Electronics Engineering
• Minimum 2 years’ relevant experience
• Technically competent in one or more of the following:
  - High-Level Architectural Modeling & Evaluation
  - VHDL/Verilog Coding & Verification
  - Logic/Test Synthesis Static Timing Analysis
  - Floorplanning, Place & Route
• Knowledge of Image Processing algorithms & applications or
  low power design techniques or Cryptographic algorithms

Application Engineers

The Job:
Support customers for Digital Set Top Box products by providing application and development consultancy, and assisting in software development and industrialisation

Requirement:
- Masters/Degree in Electronics Engineering or Computer Science
- Sound programming foundation and experience in C or C++
- Experience in real time embedded software systems (32-bit CPU + RTOS)
- Processor and peripheral architecture knowledge and debugging capability to register level

Experience in any one of the following areas is an added advantage: 1. Network programming and protocol layers 2. JAVA programming 3. Linux development platform
Senior/ DSP System Design Engineers

The job:
- Responsible in one of the following areas:
  - UMTS Protocol Software Development and Testing
  - UMTS Protocol and Cellular Terminal System Conformance Testing
  - Cellular Terminal System Software Development. Specific focus on RTOS Abstraction
  - Cellular Terminal Prototype Platform support, layer development, driver development and peripheral management
  - UMTS FDD Physical Layer Implementation/ Support

Requirements:
- Master’s/Bachelor’s Degree in Engineering (Electronics/Communications/Computers) with 1 - 3 years’ experience in GSM/GPRS/UMTS Protocol Software Development/ Test, Real Time Operating Systems such as Nucleus+, AMX, OSE, Chorus/micro or VHDL programming and FPGA prototyping
- Strong programming skills in C language
- Familiar with development tools such as Modelsim, Scirocco, FPGA Compiler II, Xilinx ISE FPGA development tools or equivalent
- Knowledge of Specification and Description Language (SDL), ARM microprocessors (ARM7, ARM9), TTCN and associated Development Tools would be advantageous